10/667,770

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6193	compar$3 and (input signal$1 with reference voltage$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
L2	27	programmable hysteresis circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
L3	6	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:40
L4	7	hysteresis delay circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L5	58833	digital-to-analog converter$1 or DAC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L6	1	l3 and l4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L7	2	l3 and l5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:15
L8	3935	714/724 or 714/727 or "714"/ 327/77 or 327/205 or 327/72 or 327/68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:39

L9	3	l3 and l8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:41

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6193	compar$3 and (input signal$1 with reference voltage$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
L2	27	programmable hysteresis circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:13
L3	6	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:40
L4	7	hysteresis delay circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L5	58833	digital-to-analog converter$1 or DAC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L6	1	l3 and l4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:14
L7	2	l3 and l5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:15
L8	3935	714/724 or 714/727 or "714"/ 327/77 or 327/205 or 327/72 or 327/68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:39

L9	3	l3 and l8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:43
L10	80314	voltage divider$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:43
L11	31740	current mirror$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L12	3342	scan chain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L13	1078	boundary scan test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L14	806	board under test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:44
L15	33103	determin$3 and noise level	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45
L16	143	hysteresis delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45

L17	1077	hysteresis voltage$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45
L18	1821	hysteresis circuit$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:45
L19	29314	default value$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L20	2840	l10 and l11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L21	4	l20 and l12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L22	1	l21 and l13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L23	1	l22 and l14 and l15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:46
L24	1	l23 and (l16 or l17 or l18 or l19)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:47

L25	6	l3 and (l16 or l17 or l18 or l19)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:47
L26	3	l25 and (l10 or l11 or l12 or l13 or l14 or l15)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/21 13:48